

FIG. 1  
(PRIOR ART)

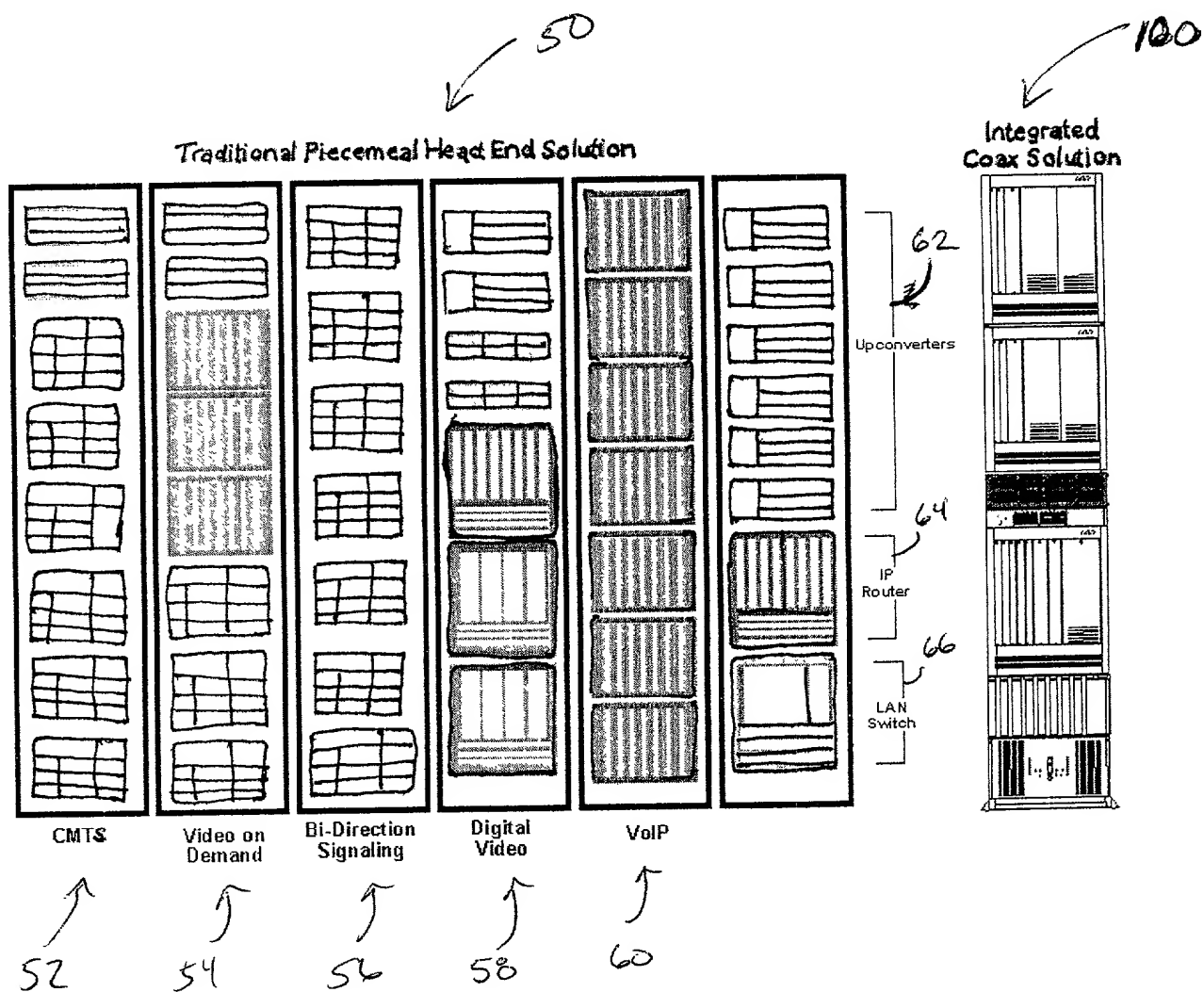


FIG. 2

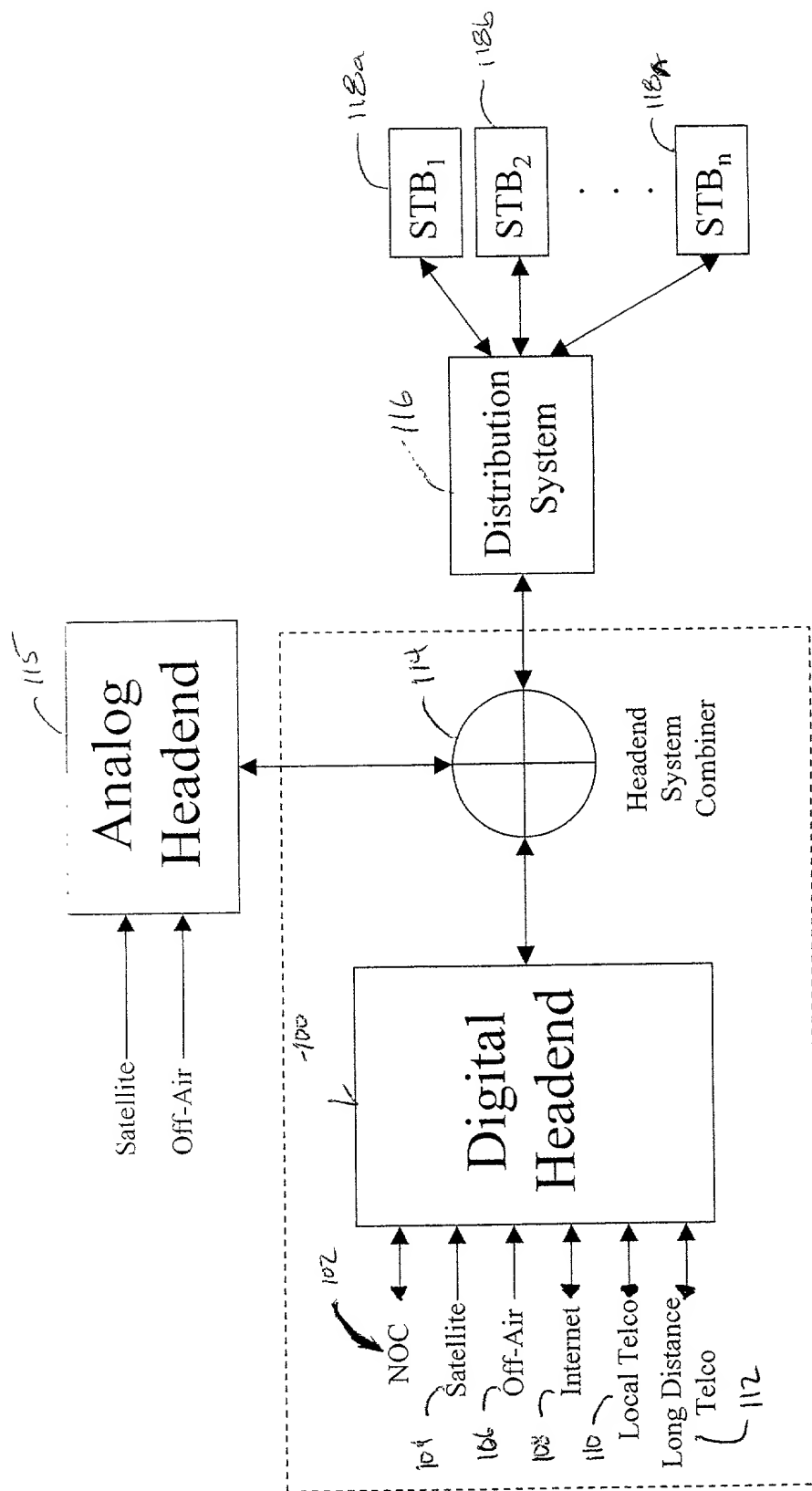
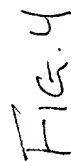


FIG. 3



**Distribution System**  $\longleftrightarrow$  **Analog Headend**

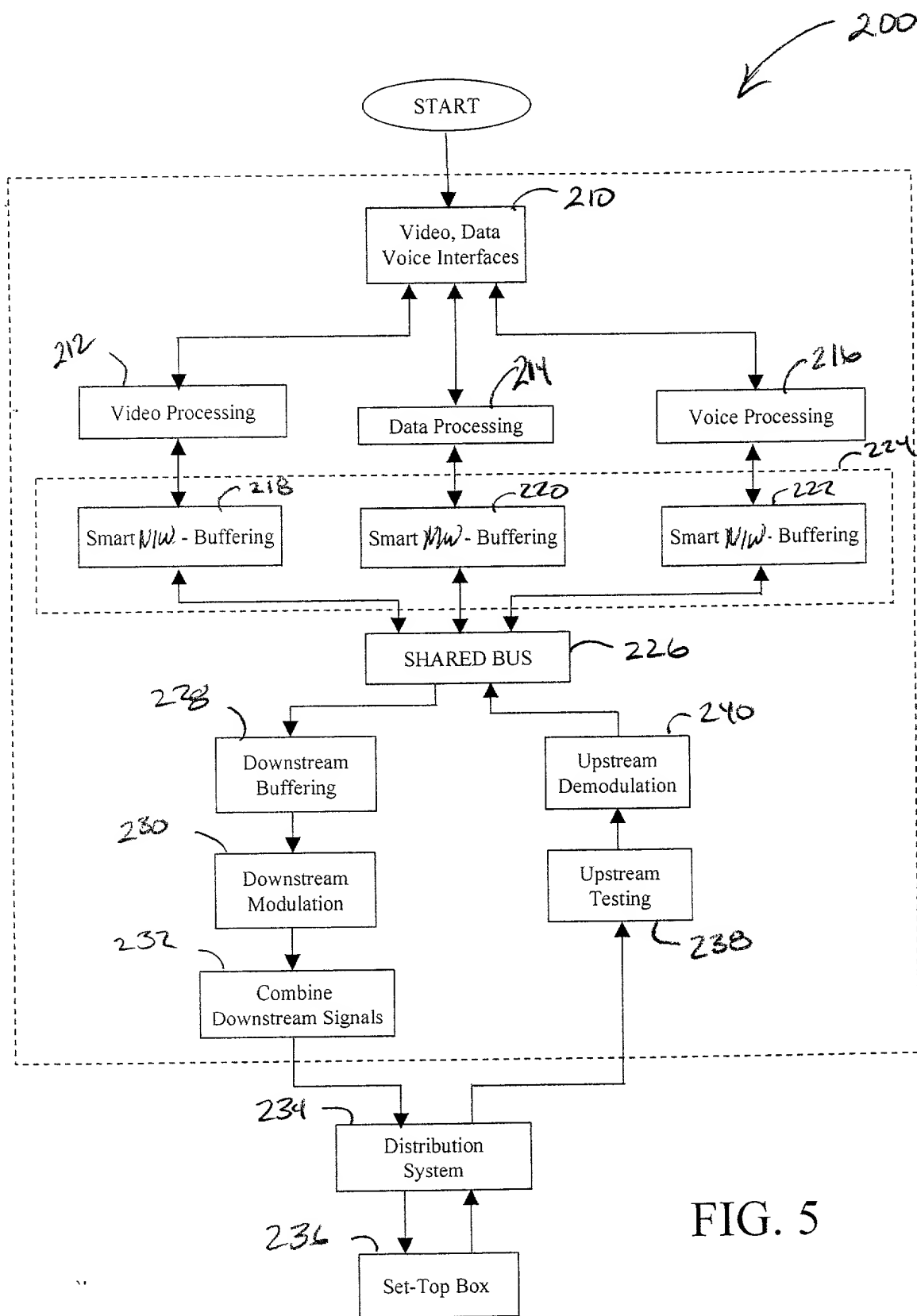


FIG. 5